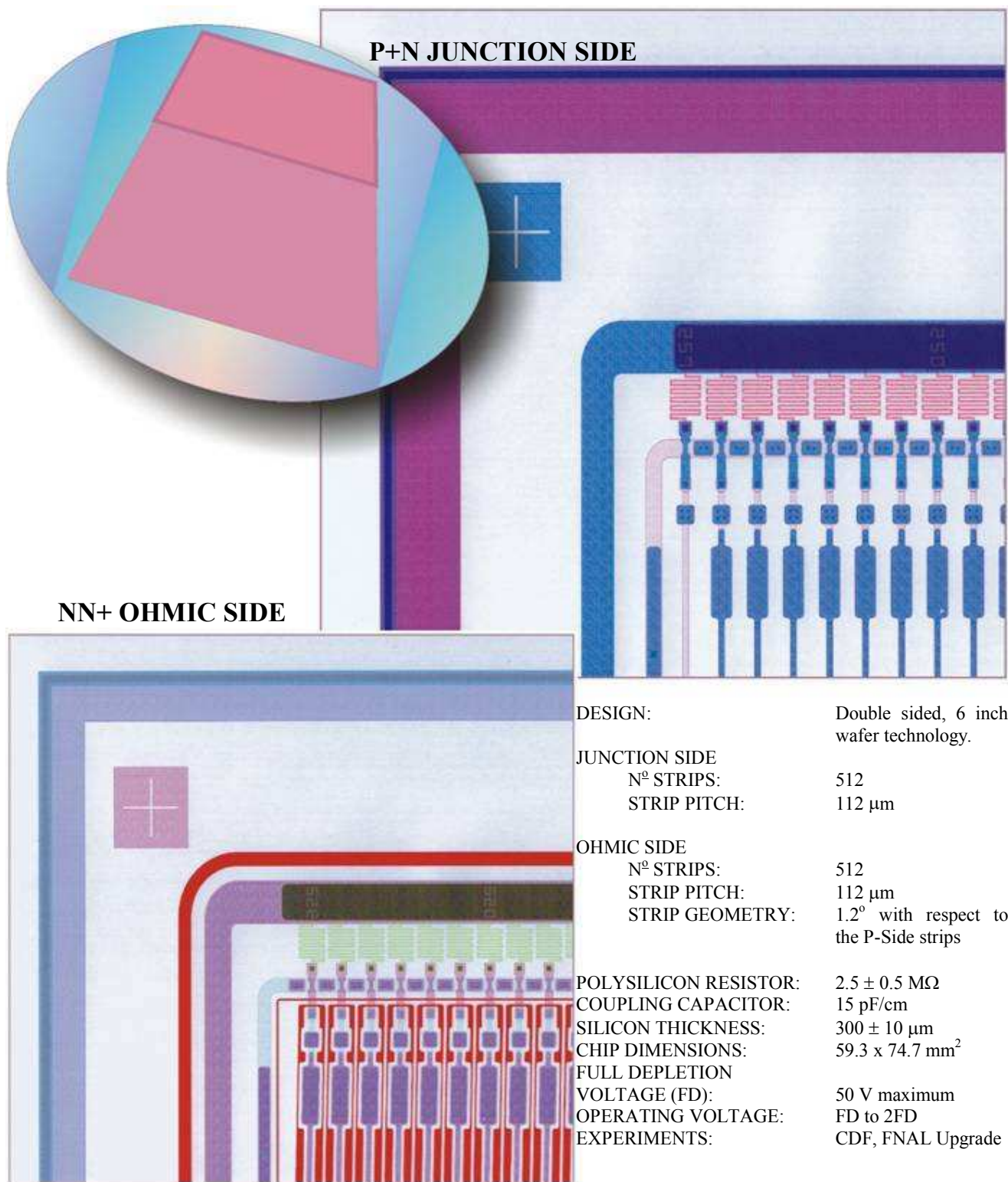


AC COUPLED ION IMPLANTED TOTALLY DEPLETED DOUBLE SIDED MICROSTRIP DETECTOR



DESIGN:	Double sided, 6 inch wafer technology.
JUNCTION SIDE	
N° STRIPS:	512
STRIP PITCH:	112 μm
OHMIC SIDE	
N° STRIPS:	512
STRIP PITCH:	112 μm
STRIP GEOMETRY:	1.2° with respect to the P-Side strips
POLYSILICON RESISTOR:	2.5 \pm 0.5 M Ω
COUPLING CAPACITOR:	15 pF/cm
SILICON THICKNESS:	300 \pm 10 μm
CHIP DIMENSIONS:	59.3 x 74.7 mm ²
FULL DEPLETION VOLTAGE (FD):	50 V maximum
OPERATING VOLTAGE:	FD to 2FD
EXPERIMENTS:	CDF, FNAL Upgrade

QUALITY ASSURANCE :ISO9001

